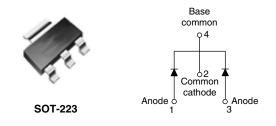


Vishay High Power Products

Schottky Rectifier, 2 x 1 A



PRODUCT SUMMARY			
I _{F(AV)}	2 x 1 A		
V _R	30 V		

FEATURES

- Small foot print, surface mountable
- Low profile
- Very low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Common cathode
- Designed and qualified for industrial level

DESCRIPTION

The 20CJQ030 surface mount Schottky rectifier series has been designed for applications requiring very low forward drop and very small foot prints. Typical applications are in portables, switching power supplies, converters, automotive system, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	2	А		
V _{RRM}		30	V		
I _{FSM}	$t_p = 5 \ \mu s \ sine$	400	А		
V _F	1 Apk, $T_J = 125 \ ^{\circ}C$ (per leg)	0.42	V		
TJ	Range	- 55 to 150	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	20CJQ030	UNITS		
DC reverse voltage	V _R	30	V		
Working peak reverse voltage	V _{RWM}	30	v		

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	per leg		50 % duty cycle at T_C = 132 °C, rectangular waveform		1	
See fig. 5	per device	I _{F(AV)}	50 % duty cycle at $T_C = 117 \circ C_c$, rectangular waveform	2	А
Maximum peak one cycle non-repetitive surge curren	at por log		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	400	A
See fig. 7	Irrent per leg		10 ms sine or 6 ms rect. pulse	rated V_{RRM} applied	24	
Non-repetitive avalanche energy per leg E_{AS} $T_J = 25 \text{ °C}, I_{AS} = 1 \text{ A}, L = 4 \text{ mH}$		2	mJ			
Repetitive avalanche curre	ent per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1	А

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	1 A	T _J = 25 °C	0.50	V
		2 A		0.59	
		1 A	T _J = 125 °C	0.42	
		2 A		0.52	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V_R = Rated V_R	0.1	mA
See fig. 2		T _J = 125 °C		15	
Typical junction capacitance per leg	CT	$V_{\rm R}$ = 5 $V_{\rm DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		120	pF
Typical series inductance per leg	LS	Measured lead to lead 5 mm from package body		6	nH
Maximum voltage rate of change	dV/dt	Rated V _R		4600	V/µs

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		- 55 to 150	°C
Maximum thermal resistance, junction to ambient	R _{thJA}		65	°C/W
Maximum thermal resistance, junction to lead	R _{thJL}	DC operation	25	
			0.13	g
Approximate weight			0.0045	oz.
Marking device		Case style SOT-223	2C.	IQE

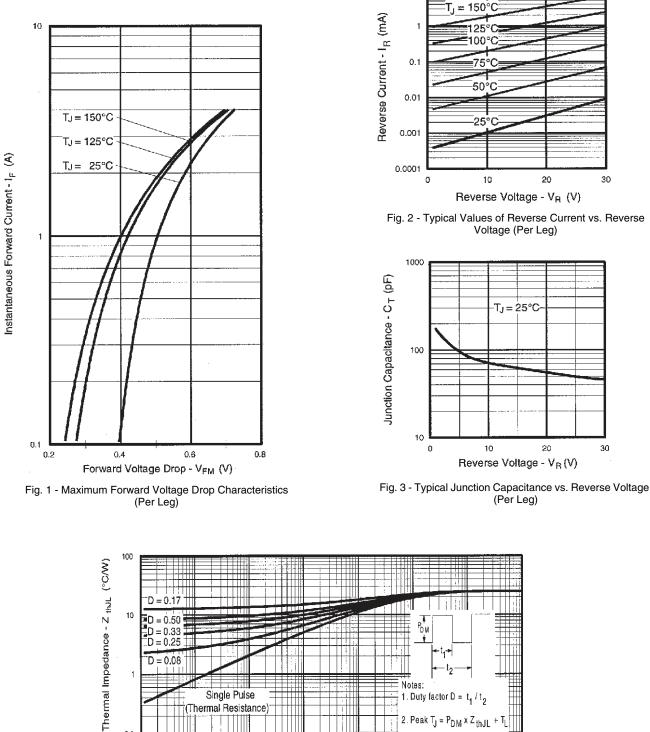
Note

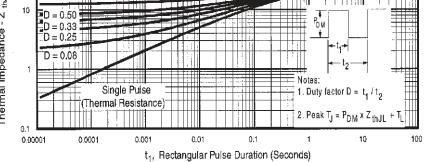
(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

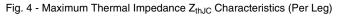


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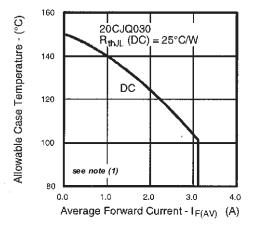


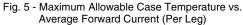


20CJQ030

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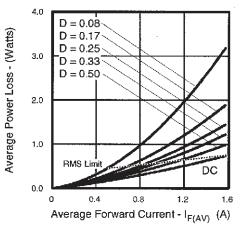


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

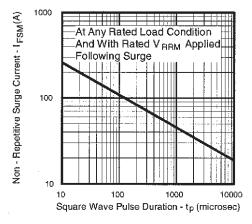


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

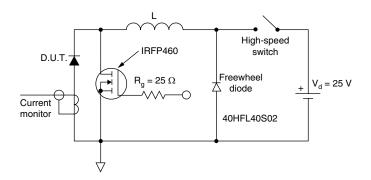


Fig. 8 - Unclamped Inductive Test Circuit

Note

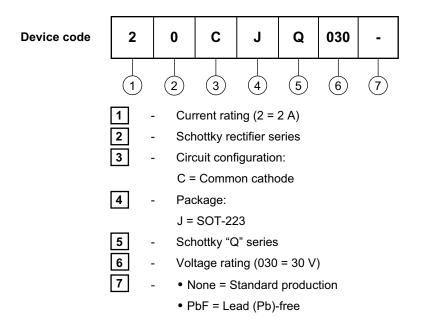
⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

 $\begin{array}{l} \mbox{Pd} = \mbox{Forward power loss} = \mbox{I}_{F(AV)} \times \mbox{V}_{FM} \mbox{ at } (\mbox{I}_{F(AV)}/\mbox{D}) \mbox{ (see fig. 6);} \\ \mbox{Pd}_{REV} = \mbox{Inverse power loss} = \mbox{V}_{R1} \times \mbox{I}_{R} \mbox{ (1 - D); I}_{R} \mbox{ at } \mbox{V}_{R1} = 80 \ \% \mbox{ rated } \mbox{V}_{R} \end{array}$



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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS			
Dimensions http://www.vishay.com/doc?95022			
Part marking information http://www.vishay.com/doc?95031			
Packaging information http://www.vishay.com/doc?95035			



Vishay

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